

Power Quality Enhancement for a Grid Connected Wind Turbine Energy System

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Abstract—A comprehensive control of a wind turbine system connected to an industrial plant is discussed in this paper, where an algorithm has been developed allowing a control structure that utilizes a four-leg inverter connected to the grid side to inject the available energy, as well as to work as an active power filter mitigating load current disturbances and enhancing power quality. A four-wire system is considered with three-phase and single-phase linear and nonlinear loads. During the connection of the wind turbine, the utility-side controller is designed to compensate the disturbances caused in presence of reactive, nonlinear, and/or unbalanced single- and intra-phase loads, in addition to providing active and reactive power as required. When there is no wind power available, the controller is intended to improve the power quality using the dc-link capacitor with the power converter attached to the grid. The main difference of the proposed methodology with respect to others in the literature is that the proposed control structure is based on the conservative power theory decompositions. This choice provides decoupled power and current references for the inverter control, offering very flexible, selective, and powerful functionalities. Real-time software benchmarking has been conducted in order to evaluate the performance of the proposed control algorithm for full real-time implementation. The control methodology is implemented and validated in hardware-in-the-loop based on the real time simulator “Opal-RT” and a TMSF28335 DSP microcontroller. The results corroborated our power quality enhancement control and allowed to exclude passive filters, contributing to a more compact, flexible, and reliable electronic implementation of a smart-grid based control.

Index Terms—Conservative power theory (CPT), four-leg voltage source converter (VSC), hardware-in-the-loop (HIL), permanent magnet synchronous generator (PMSG), power quality.

I. INTRODUCTION

THE GLOBAL capacity of installed wind turbines has rapidly increased in the last few years, by 2013 there were about 300 GW of installed wind capacity [1]. There have been

tremendous developments in the wind turbine industry supporting this energy source as a mainstream renewable resource, with competitive costs in \$/kWh when compared to traditional fossil fuel power plants. This development is due to the advancement in electrical generators and power electronics. The main issue with renewable energy is that the power is not always available when it is needed.

With the increase of power production of renewable resources, utility integration has been developed and implemented and power electronic inverters are used to control active/reactive power, frequency, and to support grid voltage during faults and voltage sags [2]–[4].

Several control approaches have been introduced in the literature for wind turbine in standalone and grid connected systems [5], [6]. The machine side controllers are designed to extract maximum power point from wind using hill-climbing control, fuzzy-based, and adaptive controllers [7], most of the time based on field-oriented or vector control approach. The grid side controllers are designed to ensure active and reactive power is delivered to the grid [8], [9].

In order to allow the theoretical framework, different power theories have been proposed and implemented in electrical power systems to analyze current and voltage components, such as the instantaneous power (PQ) theory for a three-phase system made by Akagi [10]. In PQ theory, the three-phase is transformed into a two-phase reference frame in order to extract active and reactive components in a simplified manner. A three-phase power theory in a broader perspective has been introduced, known as the conservative power theory (CPT) [11], where the current and voltage components are derived in the three-phase form, without requiring any reference-frame transformation [12]. The performance of these theories has been compared in [13] and [14].

This paper proposes a control structure in three-phase four-wire systems that provide more functionality to the grid-side converter of a wind turbine system using the CPT as an alternative to generating different current references for selective disturbances compensation, where both single- and three-phase loads are fed. Three-phase, four-wire inverters have been realized using conventional three-leg converters with “split-capacitor” or four-leg converters [15], [16]. In a three-leg conventional converter, the ac neutral wire is directly connected to the electrical midpoint of the dc bus. In four-leg converter, the ac neutral wire connection is provided through the fourth switch leg. The “four-leg” converter topology has better controllability

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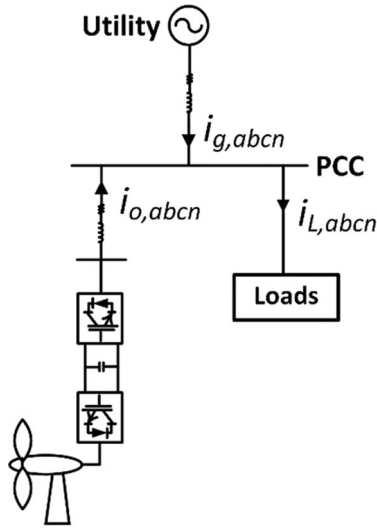


Fig. 1. Single line diagram of the addressed industrial system with wind turbine system.

than the “split-capacitor” converter topology [17]–[19]. The considered system consists of single- and three-phase linear and nonlinear (balanced and unbalanced) loads. The CPT is used to identify and to quantify the amount of resistive, reactive, unbalanced, and nonlinear characteristics of a particular load under different supply voltages condition for four-wire system. This paper is the journal version of our presented work in the 2015 Industry Applications Society (IAS) Annual Meeting [20].

The organization of the paper is as follows. Section II presents the utility-connected wind turbine system considered in this paper. In Section III, a brief review of the CPT for three-phase circuits is presented. Section IV presents the control design of the back-to-back converter system. Section V is dedicated to the experimental verification of the proposed control structure through a real-time hardware-in-the-loop (HIL) setup. Finally, the conclusion of this paper is presented in Section VI.

II. SYSTEM CONFIGURATION

Fig. 1 shows a diagram of a utility connected industrial system addressed in this paper. The structure of the power converter used in the wind turbine system is a back-to-back converter with a permanent magnet synchronous generator (PMSG) connected to the same bus with the loads. The loads are a combination of linear and highly inductive loads causing harmonics at the point of common coupling (PCC).

The model of the wind turbine system considered in this paper is described in [21]. The generator of the system is based on the PMSG. The model of the PMSG used in this paper is presented in [20].

III. CONSERVATIVE POWER THEORY

The CPT, proposed by [11], decomposes the power and current in the stationary frame, according to terms directly related to electrical characteristics, such as average power transfer, reactive energy, unbalanced loads and nonlinearities. Assuming a generic poly-phase circuit under periodic operation (period T), where \underline{v} and \underline{i} are, respectively, the voltage and current

vectors, and $\langle \hat{v} \rangle$ is the unbiased integral of the voltage vector measured at a given network port (phase variables are indicated with subscript “ m ”), the CPT authors define [13].

1) Instantaneous active power

$$p(t) = \underline{v} \cdot \underline{i} = \sum_{m=1}^M v_m i_m. \quad (1)$$

2) Instantaneous reactive energy

$$w(t) = \hat{\underline{v}} \cdot \underline{i} = \sum_{m=1}^M \hat{v}_m i_m. \quad (2)$$

The corresponding average values of (1) and (2) are the active power and reactive energy defined in (3) and (4), respectively as follows:

$$P = \bar{p} = \underline{v} \cdot \underline{i} = \frac{1}{T} \int_0^T \underline{v} \cdot \underline{i} dt = \sum_{m=1}^M P_m, \quad (3)$$

$$W = \bar{w} = \hat{\underline{v}} \cdot \underline{i} = \frac{1}{T} \int_0^T \hat{\underline{v}} \cdot \underline{i} dt = \sum_{m=1}^M W_m. \quad (4)$$

The phase currents are decomposed into three current components as follows.

Active phase currents are defined by

$$i_{am} = \frac{v_m i_m}{\|v_m\|} \quad v_m = \frac{P_m}{V_m^2} \quad v_m = G_m v_m, \quad (5)$$

where (G_m) is the equivalent phase conductance.

Reactive phase currents are given by

$$i_{rm} = \frac{\hat{v}_m i_m}{\|\hat{v}_m\|} \quad \hat{v}_m = \frac{W_m}{\hat{V}_m^2} \quad \hat{v}_m = B_m \hat{v}_m, \quad (6)$$

where (B_m) is the equivalent phase reactivity.

Void phase currents are the remaining current terms

$$\underline{i}_{vm} = \underline{i}_m - \underline{i}_{am} - \underline{i}_{rm}, \quad (7)$$

where they convey neither active power nor reactive energy.

The active and reactive phase currents can be further decomposed into balanced and unbalanced terms.

The balanced active currents have been defined as

$$\underline{i}_{am}^b = \frac{\underline{v}, \underline{i}}{\|\underline{v}\|^2} \underline{v}_m = \frac{P}{V^2} \underline{v}_m = G^b \underline{v}_m, \quad (8)$$

and such currents represent the minimum portion of the phase currents, which could be associated with a balanced equivalent circuit, responsible for conveying the total active power (P) in the circuit, under certain voltage conditions.

The balanced reactive currents have been defined as

$$\underline{i}_{rm}^b = \frac{\hat{\underline{v}}, \underline{i}}{\|\hat{\underline{v}}\|^2} \underline{\hat{v}}_m = \frac{W_r}{\hat{V}^2} \underline{\hat{v}}_m = B^b \underline{\hat{v}}_m, \quad (9)$$

and they represent the minimum portion of the phase currents, which could be associated with a balanced equivalent circuit, responsible for conveying the total reactive energy (W) in the circuit.

The imbalanced active currents are calculated by difference between (5) and (8)

$$i_{am}^u = i_{am} - i_{am}^b = (G_m - G^b) v_m. \quad (10)$$

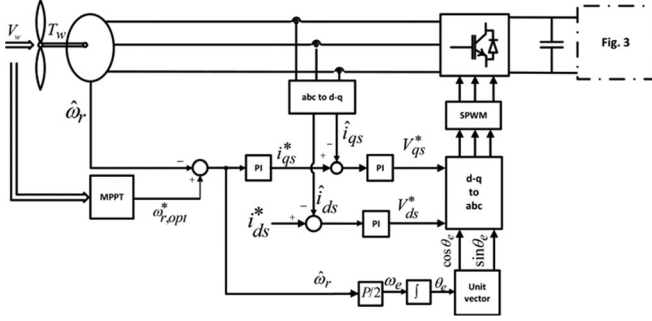


Fig. 2. Control scheme of machine side converter.

In the same way, the imbalanced reactive currents are

$$i_{rm}^u = i_{rm} - i_{rm}^b = (B_m - B^b) \hat{v}_m. \quad (11)$$

Thus, the total imbalance phase current vector is defined as

$$i_m^u = i_{am}^u + i_{rm}^u. \quad (12)$$

The current vector can be given as

$$\underline{i} = \underline{i}_a^b + \underline{i}_r^b + \underline{i}_a^u + \underline{i}_r^u + \underline{i}_v. \quad (13)$$

IV. CONTROL DESIGN

A. Machine Side Controller

The purpose of the machine side converter is to track the optimum point of the rotor to extract the maximum power existing in the turbine. For a given wind turbine, the maximum power occurs at the maximum power coefficient of the turbine [22]. For a given wind speed, there is an optimum rotor speed that gives the optimum tip speed ratio

$$\lambda_{opt} = \frac{R_w \omega_{w,opt}}{v_w}. \quad (14)$$

By knowing the tip speed ratio of the wind turbine, one can extract the maximum power from the rotor by calculating the optimum rotor speed as

$$\omega_{w,opt} = \frac{v_w \lambda_{opt}}{R_w}. \quad (15)$$

Then, this optimum rotor reference is subtracted from the measured rotor speed to produce the speed error. As shown in Fig. 2, a rotor speed controller is designed to generate the quadrature current reference to the internal current controller. The direct current reference in this paper is set to zero. The detail of the controller design procedure is presented in [23]. The parameters and values of the grid-side system and the load are illustrated in Table I.

B. Grid-Side Controller

In this section, the current-controlled voltage source inverter is designed and modeled. The control scheme for the four-leg grid-side inverter is shown in Fig. 3.

Fig. 3 illustrates the schematic diagram of the grid-tied four-leg inverter unit, consisting of a four-leg voltage source converter (VSC) and the network load that are connected to the distribution

TABLE I
PMSG PARAMETERS AND WIND TURBINE SPECIFICATIONS

Parameters	Values
Stator resistance, R_s	0.672 Ω
d-axis leakage inductance, L_d	13.74 mH
q-axis leakage inductance, L_q	13.74 mH
Flux linkage, ψ_m	2.39 Wb
Number of poles of machines, P	24
Voltage	500 V
Nominal output power of wind turbine	10 kW
Base wind speed	10 m/s
Base rotor speed	200 r/min

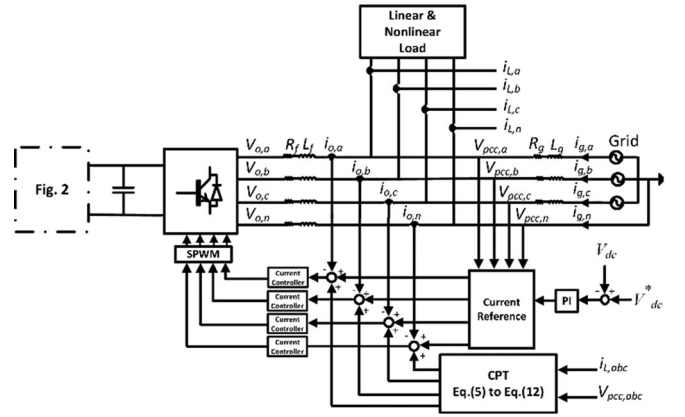


Fig. 3. Control scheme of a grid-side converter.

network at PCC. The inductance of the filter is L_f and R_f is the ohmic loss of the inductor. The machine side converter of Fig. 2 is connected in parallel with the VSC dc-link capacitor C_{dc} . It is shown that the grid-side inverter unit is controlled in an abc -reference frame. v_{pcc} is dictated by the grid representing the PCC/load voltage. The control objective is to allow the wind source to inject its available energy, as well as to work as an active power filter for improving power quality based on CPT functionalities. Fig. 4 shows the circuit, containing both balanced and unbalanced linear and nonlinear loads. The parameters and values of the grid-side system and the load are illustrated in Table II.

The inverter unit control system consists of two feedback control loops [14]. The first loop demonstrated in Fig. 5 is a fast loop controlling the output current, showing that $i_{o,abcn}$ can rapidly track their respective reference commands $i_{o,abcn}^*$, while $i_{o,n}^*$ is determined as $i_{o,n}^* = -(i_{o,a}^* + i_{o,b}^* + i_{o,c}^*)$. The outer loop depicted in Fig. 8 is a slower loop regulating the dc-link voltage. The dc-link keeps the power balance between the power which is delivered to the system in the output of the inverter and the power in the dc-link. The desired inverter output current is the summation of the active current provided from the wind (i_{active}) and the compensation of unwanted load current disturbances delivered by the CPT technique. The block diagram of the system in the “s” plane shown in Fig. 5 is designed in an abc frame based on the classical frequency response analysis method.

Consider the grid-tied four-leg inverter of Fig. 3 and the current control loop block diagram of Fig. 5; the dynamics

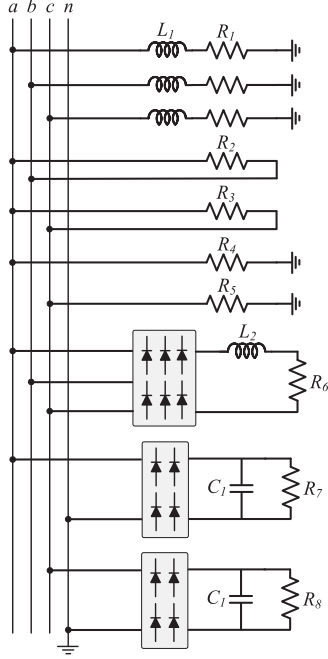


Fig. 4. Schematic diagram of the configurable load in Fig. 3.

TABLE II
GRID SIDE SYSTEM AND LOAD PARAMETERS

Parameters	Values
Nominal grid pick phase voltage, v_{pcc}	180 V
Grid frequency, f	60 Hz
Switching frequency, f_s	12 kHz
Output filter inductor, L_f	10 mH
Output filter resistor, R_f	0.1 Ω
DC link voltage reference, V_{dc}	1000 V
DC-link capacitor, C_{dc}	5 mF
Grid inductor, L_g	3 mH
Grid resistor, R_g	1 Ω
Load inductor, L_1	30 mH
Load inductor, L_2	4 mH
Load capacitor, C_1	220 μ F
Load resistor, R_1	1 Ω
Load resistor, R_2	80 Ω
Load resistor, R_3	35 Ω
Load resistor, R_4	30 Ω
Load resistor, R_5	40 Ω
Load resistor, R_6	200 Ω
Load resistor, R_7	150 Ω
Load resistor, R_8	200 Ω

of the ac-side currents $i_{o,abcn}(t)$ are described by (16) and (17). Equation (16) represents a system in which $i_{o,abcn}(t)$ are the state variables, $v_{o,abcn}(t)$ are the control inputs, and $v_{pcc,abcn}(t)$ are the disturbance inputs. Based on (17), the control input $v_{o,abcn}(t)$ are proportional to, and can be controlled by, the modulating signal $m_{o,abcn}(t)$. The transfer function of the current control scheme, $G_i(s)$, is determined as in (18). The first step to perform the controller design is to obtain the open-loop current transfer function $G_{oi}(s)$ as expressed in (19) with $C_i(s)$ the controller of the current control loop, consisting of a lag compensator as (20), where the parameters of

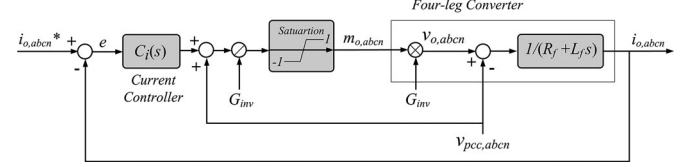


Fig. 5. Block diagram of the current control loop.

ω_z , ω_p , and k_c are the zero, pole, and the gain of the compensator, respectively. Furthermore, the voltage feed-forward compensation is employed to mitigate the dynamic couplings between the four-leg inverter and the ac system, enhancing the disturbance rejection capability of the converter system [24]

$$L_f \frac{di_{o,abcn}(t)}{dt} + R_f i_{o,abcn}(t) = v_{o,abcn}(t) - v_{pcc,abcn}(t). \quad (16)$$

$$v_{o,abcn}(t) = G_{inv} m_{o,abcn}(t) = \left(\frac{V_{dc}}{2} \right) m_{o,abcn}(t). \quad (17)$$

$$G_i(s) = \frac{i_{o,abcn}(s)}{v_{o,abcn}(s)} = \frac{1}{L_f s + R_f}. \quad (18)$$

$$G_{oi}(s) = \frac{C_i(s)}{R_f + sL_f}, \quad (19)$$

$$C_i(s) = \frac{k_c (1 + s/\omega_z)}{(1 + s/\omega_p)}. \quad (20)$$

For DSP implementation of the control system, in the next step, it is suggested to convert the functions of continuous plane “s” to the discrete plane “z”. The Plant transfer function in z-domain is obtained by means of the z-transformation. The z-transformation of a transfer function in s-domain, combined to a zero-order holder, is given by (21). Transformation is made using the relation $z = e^{sT_a}$. So, $G_i(z)$ can be defined as follows [24]:

$$G_i(z) = Z \left\{ \frac{(1 - e^{-sT_a}) G_i(s)}{s} \right\}. \quad (21)$$

$$G_i(z) = (1 - z^{-1}) Z \left\{ \frac{G_i(s)}{s} \right\}. \quad (22)$$

To allow the use of the frequency response method design, the conversion of $G_i(z)$ transfer function from “z” plane to “w” plane is performed using the bilinear transform shown in (23)

$$z = \frac{1 + \frac{T_a}{2}w}{1 - \frac{T_a}{2}w}. \quad (23)$$

From where comes

$$G_i(w) = \frac{-0.0041w + 100}{w + 10}. \quad (24)$$

Fig. 6 presents a comparison between the frequency response of system transfer function $G_i(s)$, and digitalized plant $G_i(w)$. It is noticeable that the frequency response presents conformity

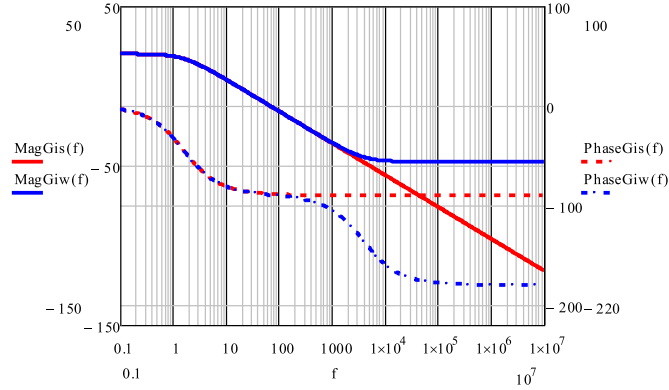


Fig. 6. Gain and the phase of the current control plant in both “s” and “w” planes.

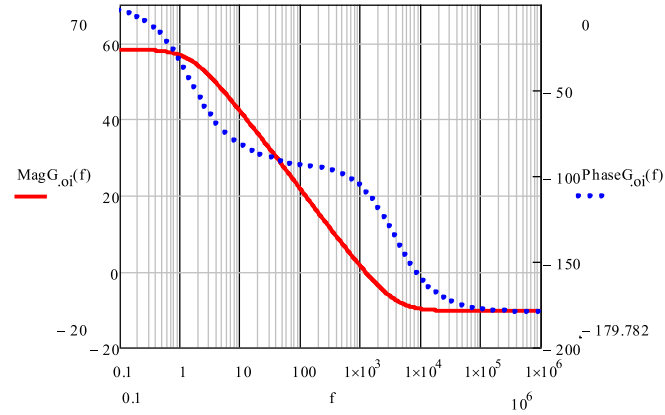


Fig. 7. Bode plot of the open-loop current transfer function.

up to 3 kHz, when the phase error goes by substantial caused by the zero added because of the digitalization process.

The crossover frequency of the current controller is chosen to be one-tenth of switching frequency. For $f_{ci} = 1.2$ kHz, $\varphi_{PMi} = 72^\circ$, and $f_z = f_{ci}/10 = 120$ Hz, the rest of parameters in (20) are calculated as $f_p = 106.6$ Hz and $k_c = 80.88$. The frequency response of the open-loop transfer function is illustrated in Fig. 7. It can be seen that at crossover frequency, the open loop gain of 0 dB and the phase margin of 72° are obtained.

The output current behavior of the grid-tied four-leg inverter can be described by (25). It can be seen that the output current only depends on the reference current. In other words, under the feed-forward compensation, the converter system is equivalent to an independent current source as viewed by the ac system

$$i_{o,abcn}(s) = \frac{C_i(s)}{L_f s + R_f + C_i(s)} i_{o,abcn}^*(s). \quad (25)$$

For digital implementation of the control system in the z-domain, the controller of (20) is discretized by the bilinear transform with a sampling time of T_s that is also the switching period [24]. Therefore, the controller transfer function $C_i(z)$

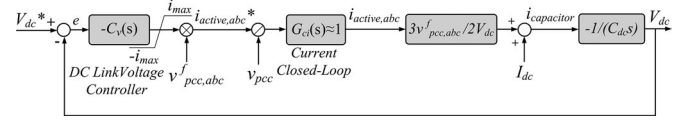


Fig. 8. Block diagram of the dc voltage control loop.

can be expressed as (26)

$$C_i(z) = \frac{72.1z - 67.7}{z - 0.94}. \quad (26)$$

The current reference, $i_{active,abc}$, is used to inject the active power delivered from the wind through the inverter. The waveform of the active current reference is defined from the fundamental component of the measured load voltage, $v_{pcc,abc}^f$, configuring sinusoidal current. Therefore, the active current is a pure sinusoidal current, in phase with the fundamental component of the instantaneous load voltage. Dimensioning of the dc-link voltage controller is determined by the transfer function between the defined current reference and the dc-link voltage.

From power balance of the inverter terminal, we have

$$P_{ac} + P_{wind} + P_{cap} = 0. \quad (27)$$

$$\frac{3}{2} v_{pcc,abc}^f i_{active,abc} + V_{dc} I_{dc} + V_{dc} i_{capacitor} = 0. \quad (28)$$

where $i_{capacitor}$ is the dc-link capacitor current and $3/2$ factor comes from the average ac power flow using peak values and $v_{pcc,abc}^f$ represents the fundamental component of the PCC voltage.

From (28) the current through the capacitor is

$$i_{capacitor} = - \left(\frac{3v_{pcc,abc}^f i_{active,abc}}{2V_{dc}} + I_{dc} \right). \quad (29)$$

The same current in terms of voltage across the capacitor is given by

$$C_{dc} \frac{dV_{dc}}{dt} = i_{capacitor}. \quad (30)$$

From (29) and (30), the differential equation for the dc-link voltage becomes

$$\frac{dV_{dc}}{dt} = - \frac{1}{C_{dc}} \left(\frac{3v_{pcc,abc}^f i_{active,abc}}{2V_{dc}} + I_{dc} \right). \quad (31)$$

Based on (31) the dc voltage is regulated by controlling the active current $i_{active,abc}$. The block diagram of the dc voltage control loop is shown in Fig. 8. The dc-link voltage controller $C_{vdc}(s)$ is multiplied by -1 to compensate for the negative sign of dc bus voltage dynamics. We will select the bandwidth of dc voltage loop to be less than two orders of magnitude smaller than that of the current loop.

Therefore, the closed current loop can be assumed ideal for design purposes and replaced by unity. The transfer functions of dc-link voltage control scheme, $G_{vdc}(s)$, is presented in (32). The open-loop transfer functions of the dc voltage control loop, $G_{ovdc}(s)$, is presented in (33) with $C_v(s)$ the controller

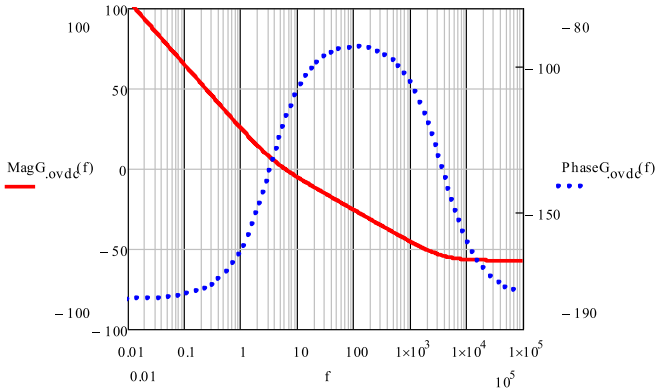


Fig. 9. Bode plot of the open loop dc-link voltage transfer function.

of the dc voltage control loop, consisting of a proportional integral compensator as in (34), where the parameters of k_p and k_i are the proportional and integral gains of the compensator, respectively,

$$G_{vdc}(s) = \frac{3v_{pcc,abc}^f}{2V_{dc}} \frac{1}{C_{dc}s}. \quad (32)$$

$$G_{ovdc}(s) = C_v(s) G_{vdc}(s) \quad (33)$$

$$C_v(s) = \frac{k_p s + k_i}{s}. \quad (34)$$

For DSP implementation of the dc-link voltage control scheme, $G_{vdc}(s)$, is converted from continuous plane “s” to the discrete plane “z” in (35). To allow the use of frequency response method design, the conversion of $G_{vdc}(z)$ transfer function from “z” plane to “w” plane in (36) is performed, using the bilinear transform of (23)

$$G_{vdc}(z) = (1 - z^{-1}) Z \left\{ \frac{G_{vdc}(s)}{s} \right\}. \quad (35)$$

$$G_{vdc}(w) = \frac{-0.00225w + 54}{w}. \quad (36)$$

The crossover frequency of the dc voltage loop is chosen to be $f_{cvdc} = 6$ Hz and the phase margin φ_{PMvdc} is selected to be 60° . We can compute that $k_p = 0.6$ and $k_i = 13.12$. Fig. 9 shows the frequency response of the open-loop dc-link voltage control scheme. It can be seen that at cross over frequency, the open loop gain of 0 dB and the phase margin of 60° are obtained.

The dc-link voltage controller $C_v(s)$ is also discretized for digital implementation using the bilinear transform with a sampling time of T_s that is also the switching period [24]. Therefore, the controller transfer function $C_v(z)$ can be expressed as follows:

$$C_v(z) = \frac{0.6z - 0.6}{z - 1}. \quad (37)$$

V. REAL-TIME SIMULATION AND HARDWARE IN THE LOOP RESULTS

The proposed industrial system with the wind turbine shown in Fig. 1 was modeled and compiled using the well-known real-time simulator Opal-RT. Opal-RT allows precise bench-

marking of real-time controllers, with specific sampling for specific control blocks. After an Opal-RT study such as this one, it is possible to generate C code for accurate compilation on real-time kernels or real-time operating systems, usually implemented with DSP hardware.

The PMSG parameters used in the model are taking from [25]. The specification of the wind turbine was selected in accordance to those parameters. The wind turbine has an optimum wind speed of 200 r/mIN at 10-m/s rated wind speed. The load parameters listed in Table II are used in the model.

The control algorithm was implemented using HIL. The wind turbine model with the grid connected back-to-back converter and power grid were built inside Matlab/Simulink. Then, the system was compiled inside the real-time simulator “Opal-RT.” The control of the grid-side converter was implemented in the co-processor DSP (TI TMS230F28335), outside the Opal-RT system. The CPT theory was coded inside the DSP along with the current controllers. A sampling frequency of 12 kHz is used to discretize the signals. The test is implemented for various cases. The scaling for phase voltages, phase, and neutral currents per division are 60 V, 15 A and 5 A, respectively.

A. Active Power Delivery

In this case study, the four-leg inverter is set to deliver active power produced by the wind to the load, ($i_{ref} = i_{active}$), and the remaining active power is delivered to the grid with unity power factor without doing any compensation strategy. Fig. 10(a) shows the inverter voltage is in phase with the inverter current meaning only active power is delivered to the load and grid. From Fig. 10(b), the grid currents are unbalanced and distorted showing the requirement for power quality improvement. In Fig. 10(c), it is clear that the utility is supplying the linear and nonlinear single loads through its neutral wire while the inverter neutral current is zero.

B. Active and Reactive Power Delivery

In Fig. 11, the controller is set to supply the balance reactive current/power component of the load besides the delivery of active power ($i_{ref} = i_{active} + i_r^b$). From the voltage and current waveforms shown in Fig. 11(a), the inverter is supplying active and reactive power since the inverter current is no longer in phase with the voltage. The result of this compensation strategy is shown in Fig. 11(b) in which the void and unbalance current components of the load is supplied by the grid. It can be seen from Fig. 11(c) the grid is supplying the neutral current, related to single phase loads.

C. Active Power Delivery and Unbalance Compensation

The load considered in the system imposes unbalance component to the grid’s current. Therefore, the CPT, proposed in the paper, is used to extract the unbalance current/power component of the load. In this study, the aim is to compensate the unbalance current component caused by the single- and intraphase loads ($i_{ref} = i_{active} + i^u$). Therefore, the inverter current is sinusoidal but unbalanced whereas the grid currents are

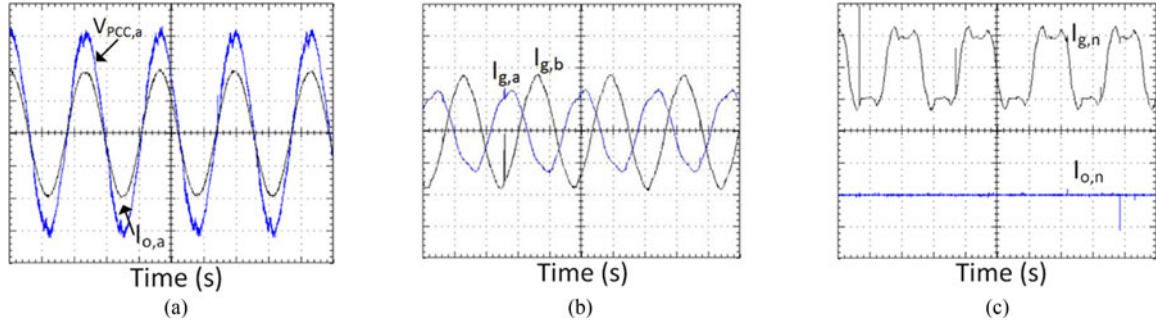


Fig. 10. Active power delivery: (a) PCC voltage and inverter current; (b) two phases of grid currents; and (c) grid neutral current ($I_{g,n}$) and inverter neutral current ($I_{o,n}$).

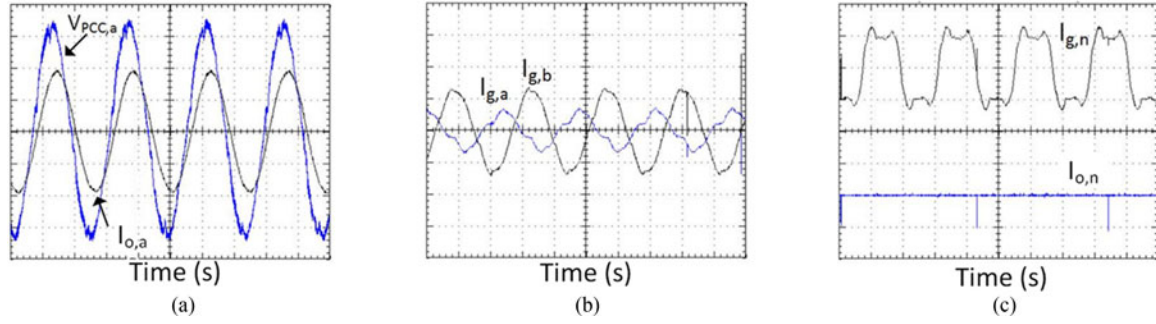


Fig. 11. Active and reactive power delivery: (a) PCC voltage and inverter current; (b) two phases of grid currents; and (c) grid neutral current ($I_{g,n}$) and inverter neutral current ($I_{o,n}$).

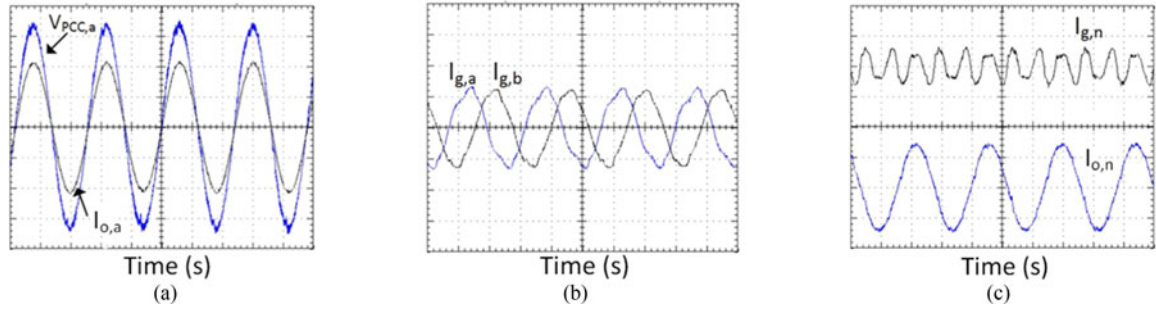


Fig. 12. Active power delivery and unbalance compensation: (a) PCC voltage and inverter currents; (b) two phases of grid current; and (c) grid neutral current ($I_{g,n}$) and inverter neutral current ($I_{o,n}$).

balanced but nonsinusoidal and out of phase with the voltages as shown in Fig. 12(a) and (b) respectively. In this case, the inverter current is responsible for supplying unbalance current component of the single phase loads through its fourth-leg as it is illustrated in Fig. 12(c). Note that the harmonic current component of the single- and three-phase loads is still supplied by the grid.

D. Active Power Delivery and Harmonics Compensation

At this case study, the inverter is providing harmonics compensation by injecting the void currents ($i_{\text{ref}} = i_{\text{active}} + i_v$). From Fig. 13(a) and (b), the inverter current is nonlinear whereas the grid current is sinusoidal but unbalanced and not in phase with the voltages. It can be observed that the grid in this case is

not supplying the single-phase void current components through its neutral wire rather it is supplied by the inverter through its fourth-leg as illustrated in Fig. 13(c). The neutral wire of the grid carries only the unbalance current component related to the single-phase loads.

E. Active Power Delivery and Nonactive Compensation

In Fig. 14, the inverter is set to compensate nonactive current component of the load current including all disturbances, i.e., load reactive power, nonlinearities, and unbalances ($i_{\text{ref}} = i_{\text{active}} + i_{na}$). Fig. 14(a) shows that the inverter current contains nonactive current component, whereas Fig. 14(b) shows the grid is absorbing the remaining active current which is not consumed by the load. Note that the active current, exported

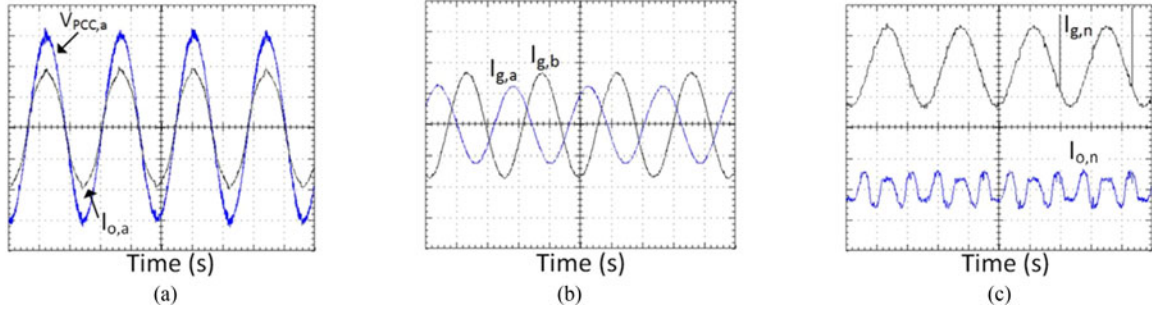


Fig. 13. Active power delivery and void compensation: (a) PCC voltage and inverter currents; (b) two phases of grid current; and (c) grid neutral current ($I_{g,n}$) and inverter neutral current ($I_{o,n}$).

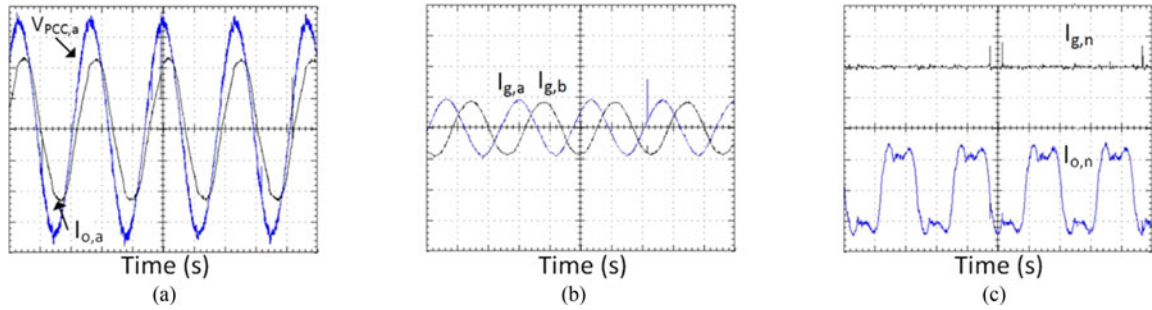


Fig. 14. Active power delivery and nonactive compensation: (a) PCC voltage and inverter currents; (b) two phases of grid current; and (c) grid neutral current ($I_{g,n}$) and inverter neutral current ($I_{o,n}$).

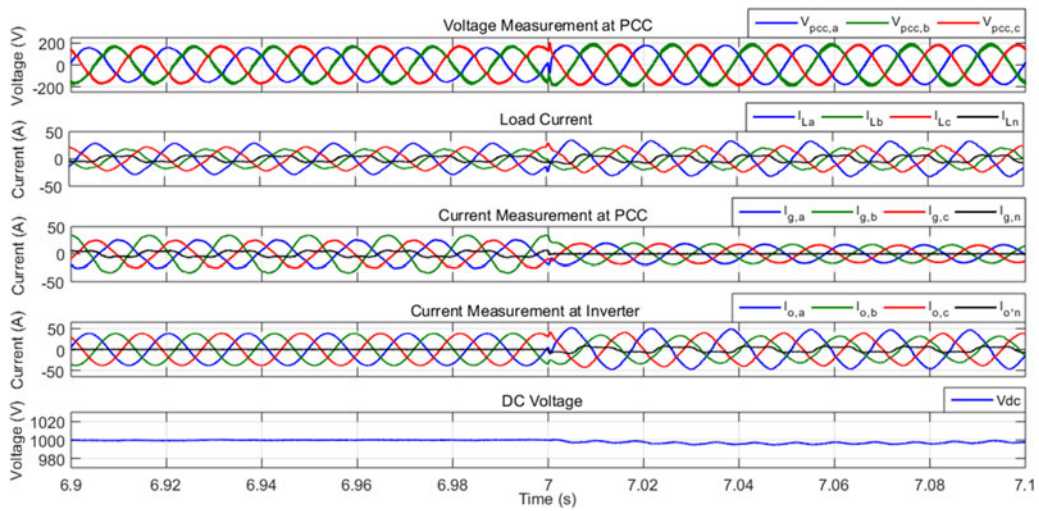


Fig. 15. Active power delivery and nonactive compensation at $t = 7$ s.

to the grid is proportional to the instantaneous PCC voltages. As shown in Fig. 14(c), the grid supplies zero current through its neutral and the inverter is supplying the return current of single phase loads through its fourth-leg.

F. Multifunctional and Active Filter Modes

In this section, two different tests are performed to validate the overall performance of the machine side and the grid side controllers during different wind speed conditions.

In Fig. 15, a test is done to validate the controller when it switches from active power delivery only to active and nonactive compensation at maximum wind power. From Fig. 15, at $t = 7$ s, the inverter started providing active power as well as nonactive compensation. The dc-link voltage starts to oscillate but kept at its desired value. The grid current becomes sinusoidal and balanced. The inverter current, on the other hand, becomes unbalanced and nonlinear. The neutral current is produced by the fourth leg of the inverter resulting in zero neutral current at the grid side.

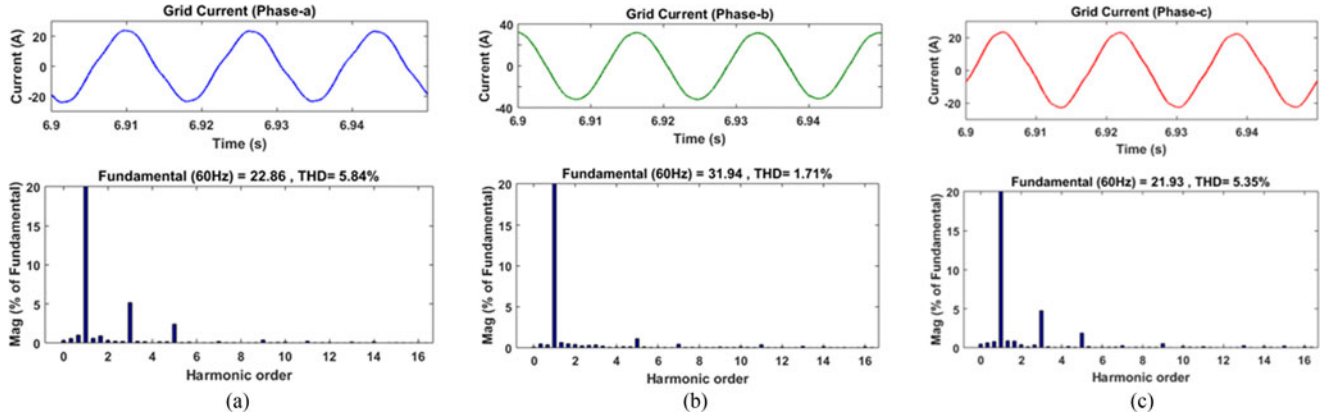


Fig. 16. Spectrum and THD of grid current without power quality improvement: (a) phase-a; (b) phase-b; and (c) phase-c.

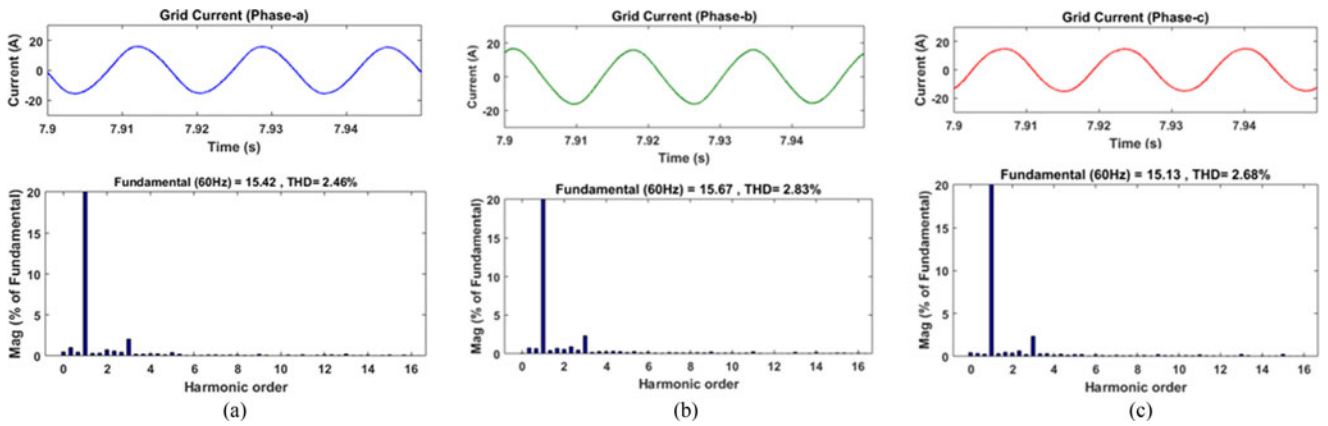


Fig. 17. Spectrum and THD of grid current with nonactive compensation: (a) phase-a; (b) phase-b; and (c) phase-c.

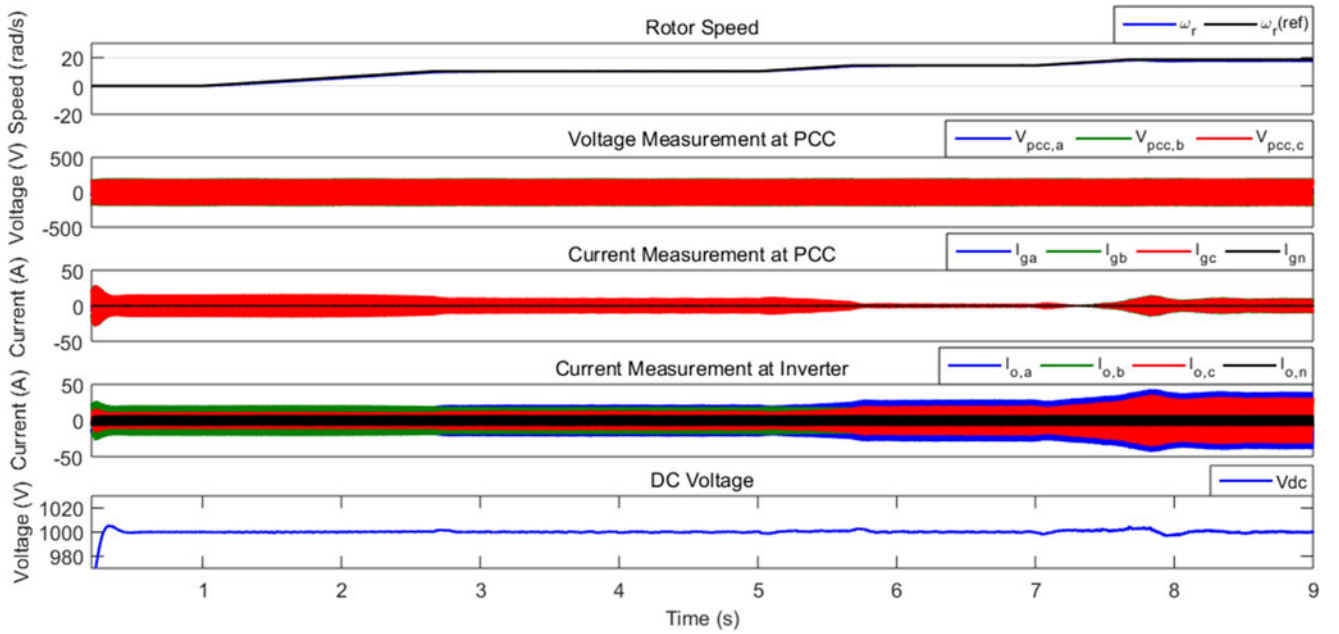


Fig. 18. Active power delivery and nonactive compensation under different wind speeds.

The harmonics spectrum of the grid current and total harmonic distortion (THD) with no compensation is shown Fig. 16. Since there are single- and three-phase loads as well as intraphase loads in the system, the THD is different for each phase. The current of phase-a and phase-c contain THD of 5.84% and 5.35%, respectively.

In Fig. 17, the grid current spectrum is demonstrated after the inverter is set to compensate the load nonactive current components. The THD of phases-a and phase-c were reduced from 5.84% and 5.35% to about 2.46% and 2.68%, respectively. Phase-b initially had much less harmonics because it does not have nonlinear single-phase load as the other phases. The amplitude of the grid current is reduced as the inverter is also supplying the unbalance components.

In Fig. 18, a comprehensive test is performed under different wind speeds. At no wind available or zero rotor speed, the grid-side inverter is operating as active filter. Therefore, the controller is intended to keep the dc voltage at constant value (1000 V) and provide nonactive compensation to improve the power quality of the grid current. During this condition, the grid supplies the active power for the load. When the wind speed increases above the cut-in speed and the turbine started producing power, the grid-side inverter inject the active and compensate nonactive components. If the produced power is more than the load power, the remaining is injected to the grid. During all the time, the dc-link voltage is kept constant at 1000 V.

VI. CONCLUSION

This paper addressed a comprehensive control method for a back-to-back wind turbine system connected to an industrial plant. The control uses the four-leg inverter at the grid side to supply available active power from the wind turbine system along with full compensation of load current disturbances. The main contribution is based on CPT to impress the set-point reference and impose disturbances mitigation, which adds significant flexibility to the control structure.

The control structure was tested with a comprehensive real-time benchmarking case-study with hardware in the loop. The control algorithms were compiled inside our TI DSP and validated using the real-time system "Opal-RT." The algorithms were debugged and are ready for experimental validation in a retrofitting of a wind turbine (future work). The results showed good performance of the algorithm and the THD was improved for all different operation conditions. The results support the system presented here which can avoid installation of active filter hardware by the utility or by the industrial consumer.

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